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In re Application of: Gary D. Carpenter *et al.*

Serial No.: 10/064,708

Filing Date: 08/08/2002

Art Unit: 2819

Dkt. No.: BUR920020011

Title: **LOW POWER LOW VOLTAGE TRANSISTOR-TRANSISTOR LOGIC/IO DRIVER**

Commissioner for Patents
Office of Initial Patent Examination
Customer Service Center
Washington, D.C. 20231

CORRECTION OF FILING RECEIPT

Sir:

Applicants respectfully request correction of the **Corrected Filing Receipt** for this case as indicated on the attached copy of the filing receipt.

Consideration of the foregoing in relation to this patent application is respectfully requested.

Respectfully submitted,

Arlen L. Olsen

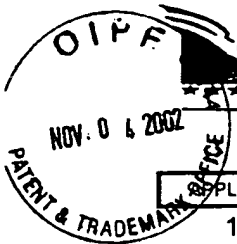
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Washington, DC 20231
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/064,708	08/08/2002	2819	740	BUR920020011	2	20	3

 030449
SCHMEISER, OLSEN + WATTS
SUITE 201
3 LEAR JET
LATHAM, NY 12033

CONFIRMATION NO. 1884

CORRECTED FILING RECEIPT

OC000000008907847

Date Mailed: 10/04/2002

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)
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**Assignment For Published Patent Application**

International Business Machines Corporation, Armonk, NY;

Domestic Priority data as claimed by applicant**Foreign Applications**

If Required, Foreign Filing License Granted 08/21/2002

Projected Publication Date: 02/12/2004

Non-Publication Request: No

Early Publication Request: No

Title

Low power low voltage transistor-transistor logic I/O driver

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 SCHMEISER, OLSEN
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Preliminary Class

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